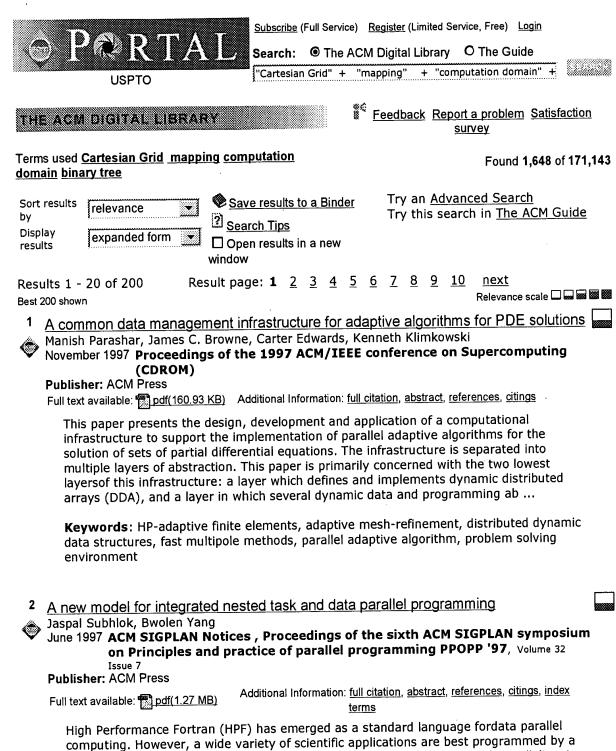
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combination of task and data parallelism. Therefore, a good model of task parallelism is important for continued success of HPF for parallel programming. This paper presents a task parallelism model that is simple, elegant, and relatively easy to implement in an HPF environment. Task parallelism is exploited by mechanisms for di ...

Regular expression pattern matching for XML

Haruo Hosoya, Benjamin Pierce

January 2001 ACM SIGPLAN Notices , Proceedings of the 28th ACM SIGPLAN-SIGACT

symposium on Principles of programming languages POPL '01, Volume 36 Issue 3 Publisher: ACM Press Additional Information: full citation, abstract, references, citings, index Full text available: pdf(1.25 MB) <u>terms</u> We propose regular expression pattern matching as a core feature for programming languages for manipulating XML (and similar tree-structured data formats). We extend conventional pattern-matching facilities with regular expression operators such as repetition (*), alternation (I), etc., that can match arbitrarily long sequences of subtrees, allowing a compact pattern to extract data from the middle of a complex sequence. We show how to check standard notions of exhaustiveness and $\boldsymbol{r} \dots$ 4 Direct numerical simulation of turbulence with a PC/linux cluster: fact or fiction? G.-S. Karamanos, C. Evangelinos, R. C. Boes, R. M. Kirby, G. E. Karniadakis January 1999 Proceedings of the 1999 ACM/IEEE conference on Supercomputing (CDROM) **Publisher: ACM Press** Full text available: pdf(1.38 MB) Additional Information: full citation, references, citings, index terms Technical reports SIGACT News Staff January 1980 ACM SIGACT News, Volume 12 Issue 1 **Publisher: ACM Press** Full text available: pdf(5.28 MB) Additional Information: full citation Loop optimization for a class of memory-constrained computations D. Cociorva, J. W. Wilkins, C. Lam, G. Baumgartner, J. Ramanujam, P. Sadayappan June 2001 Proceedings of the 15th international conference on Supercomputing Publisher: ACM Press Additional Information: full citation, abstract, references, citings, index Full text available: pdf(160.59 KB) terms Compute-intensive multi-dimensional summations that involve products of several arrays arise in the modeling of electronic structure of materials. Sometimes several alternative formulations of a computation, representing different space-time trade-offs, are possible. By computing and storing some intermediate arrays, reduction of the number of arithmetic operations is possible, but the size of intermediate temporary arrays may be prohibitively large. Loop fusion can be applied to reduce memor ... 7 Applications and problem solving environments: Enhancing scalability of parallel structured AMR calculations Andrew M. Wissink, David Hysom, Richard D. Hornung June 2003 Proceedings of the 17th annual international conference on Supercomputing

Full text available: pdf(312.11 KB) Additional Information: full citation, abstract, references, index terms We discuss parallel performance of structured adaptive mesh refinement calculations using the SAMRAI library. We focus on fundamental aspects of adaptive gridding and dynamic computation of changing data dependencies. Previous analysis of performance of

large-scale parallel adaptive calculations revealed poor scaling in these operations. Specifically, we found that these operations are inexpensive for small problems, but that

Publisher: ACM Press

Keywords: graph embeddings, network emulations, parallel architectures, processor arrays

13	Work-preserving emulations of fixed-connection networks						
٠	R. Koch, T. Leighton, B. Maggs, S. Rao February 1989 Proceedings of the twenty-first annual ACM symposium on Theory of computing						
	Publisher: ACM Press						
	Full text available: pdf(1.77 MB) Additional Information: full citation, abstract, references, citings, index terms						
	In this paper, we study the problem of emulating TG steps of an NG-node guest network on an NH-node host network. We call an emulation work-preserving if the time required by the host, TH, is &Ogr(TGNG/NH) becau						
14	The physical mapping problem for parallel architectures Lenwood S. Heath, Arnold L. Rosenberg, Bruce T. Smith June 1988 Journal of the ACM (JACM), Volume 35 Issue 3						
	Publisher: ACM Press						
	Full text available: pdf(2.30 MB) Additional Information: full citation, abstract, references, citings, index terms						
	The problem of realizing an idealized parallel architecture on a (possibly fault-laden) physical architecture is studied. Our formulation performs the mapping in the light of the algorithm that one wants to implement on the idealized architecture. A version of the mapping algorithm suggested by the DIOGENES methodology for designing fault-tolerant VLSI processor arrays is settled definitely. Two quality metrics for mappings are considered, the first embodying an idealized notion of						
15	Research papers: graph and tree-structured data: Similarity evaluation on tree-						
•	structured data Rui Yang, Panos Kalnis, Anthony K. H. Tung June 2005 Proceedings of the 2005 ACM SIGMOD international conference on Management of data Publisher: ACM Press	bosocood					
	Full text available: pdf(501.04 KB) Additional Information: full citation, abstract, references						
	Tree-structured data are becoming ubiquitous nowadays and manipulating them based on similarity is essential for many applications. The generally accepted similarity measure for trees is the edit distance. Although similarity search has been extensively studied, searching for similar trees is still an open problem due to the high complexity of computing the tree edit distance. In this paper, we propose to transform tree-structured data into an approximate numerical multidimensional vector which						
16	Technology decomposition and mapping targeting low power dissipation						
٠	Chi-Ying Tsui, Massoud Pedram, Alvin M. Despain July 1993 Proceedings of the 30th international conference on Design automation	2000000					
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	Full text available: 📆 pdf(657.86 KB) Additional Information: full citation, references, citings, index terms						
17	Level set and PDE methods for computer graphics David Breen, Ron Fedkiw, Ken Museth, Stanley Osher, Guillermo Sapiro, Ross Whitaker August 2004 Proceedings of the conference on SIGGRAPH 2004 course notes GRAPH						
	'04 Publisher: ACM Press						

Full text available: 🎇 pdf(17.07 MB) Additional Information: full citation, abstract

Level set methods, an important class of partial differential equation (PDE) methods, define dynamic surfaces implicitly as the level set (iso-surface) of a sampled, evolving nD function. The course begins with preparatory material that introduces the concept of using partial differential equations to solve problems in computer graphics, geometric modeling and computer vision. This will include the structure and behavior of several different types of differential equations, e.g. the level set eq ...

18	Programming	with	abstract	data types,	some examples

Bengt Nordström

January 1978 Proceedings of the 1978 annual conference - Volume 2

Publisher: ACM Press

Full text available: pdf(600.43 KB) Additional Information: full citation, abstract, references, index terms

The data structuring facilities of contemporary languages like Algol-68, Pascal and Simula supports only one type of graphs, the general graph. It is only possible to define the structure of a node in a graph, not to define how the nodes are interrelated. Nothing in the languages prevents the programmer from using for instance a node in a binary tree as an element in a double list. This means that the programmer has to prove a lot of invariants on graphs which would n ...

Keywords: Graphs, Mappings, Pascal, Pointers

19 QuickStore: a high performance mapped object store

Seth J. White, David J. DeWitt

May 1994 ACM SIGMOD Record , Proceedings of the 1994 ACM SIGMOD international conference on Management of data SIGMOD '94, Volume 23 Issue 2

Publisher: ACM Press

Full text available: pdf(1,73 MB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u>

This paper presents, QuickStore, a memory-mapped storage system for persistent C++ built on top of the EXODUS Storage Manager. QuickStore provides fast access to in-memory objects by allowing application programs to access objects via normal virtual memory pointers. The paper also presents the results of a detailed performance study using the OO7 benchmark. The study compares the performance of QuickStore with the latest implementation of the E programming language. These systems exemplify ...

20 Special system-oriented section: the best of SIGMOD '94: QuickStore: a high

performance mapped object store

Seth J. White, David J. DeWitt

October 1995 The VLDB Journal — The International Journal on Very Large Data
Bases, Volume 4 Issue 4

Publisher: Springer-Verlag New York, Inc.

Full text available: pdf(2.58 MB)
Additional Information: full citation, abstract, references, citings

QuickStore is a memory-mapped storage system for persistent C++, built on top of the EXODUS Storage Manager. QuickStore provides fast access to in-memory objects by allowing application programs to access objects via normal virtual memory pointers. This article presents the results of a detailed performance study using the OO7 benchmark. The study compares the performance of QuickStore with the latest implementation of the E programming language. The QuickStore and E systems exemplify the two ba ...

Keywords: benchmark, client-server, memory-mapped, object-oriented, performance, pointer swizzling

Results (page 1): "Cartesian Grid" + "mapping" + "computation domain" + "binary tree" Page 6 of 6

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AF Blumberg, BN Kim, SO'Neil, JK Lewis, PJ Stein ... - hydroqual.com ... The offshore extent of the modeled **domain** extends out to ... this problem by creating a mathematical **mapping** that went ... from one node in the quad **tree** to another. ... <u>Cited by 1 - View as HTML - Web Search</u>

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N Ray, ST Acton - IEEE TRANSACTIONS ON IMAGE PROCESSING, 2005 - ieeexplore.ieee.org ... connected sets—, such that and, in the ad- jacency tree, is the parent ... 4) Increasing Criterion: Let be the image domain, then a function mapping the power ...

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Remote Interactive Direct Volume Rendering of AMR Data - group of 11 » O Kreylos, GH Weber, EW Bethel, JM Shalf, B Hamann ... - IEEE Visualization 2002, Boston, MA (US), 10/27/2002–11/01/ ..., 2002 - cipic.ucdavis.edu ... to perform load bal- ancing during domain decomposition that ... renderers employ the 3D texture mapping capabilites of ... we use a simple binary-tree based composit ... Cited by 3 - View as HTML - Web Search

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NA Petersson - SIAM J. SCI. COMPUT, 1999 - epubs.siam.org ... on a combination of oc- trees and **binary** space partition ... regard each component grid as a **mapping** from the ... in parameter space to the physical **domain** covered by ... <u>Cited by 10 - Web Search - BL Direct</u>

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J Steensland, S Chandra, M Parashar - IEEE Transactions on Parallel and Distributed Systems, 2002 - doi.ieeecomputersociety.org

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NA Petersson - SIAM JOURNAL ON SCIENTIFIC COMPUTING, 1999 - epubs.siam.org
... are not on the boundary of the computational **domain** and must ... These nodes are the leaves of the quad **tree**. To invert the surface grid **mapping** for a point x ...

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